

Application Data Sheet

Application Information

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| Application Type:: | Regular |
| Subject Matter:: | Utility |
| Title:: | Programmable Analog Bias Circuits Using Floating Gate Cmos Technology |
| Attorney Docket Number:: | CAT-00001 |
| Request for Early Publication?:: | No |
| Request for Non-Publication?:: | No |
| Suggested Drawing Figure:: | 1 |
| Total Drawing Sheets:: | 4 |
| Small Entity:: | Yes |
| Petition included?:: | Yes |

Inventor Information

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|-------------------------------|-----------------------------------|
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